

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Milivoje Aleksic et al.

Serial No. 10/075,149

Filing Date: February 14, 2002

Confirmation No.: 7928

Examiner: Justin King

Art Group: 2111

Our File No.: 00100.01.0060

Docket No.: 00100.02.0060 (990060D-1)

Title: SYSTEM OF ACCESSING DATA IN A GRAPHICS SYSTEM AND

METHOD THEREOF

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Christine Wright

INFORMATION DISCLOSURE STATEMENT IN ACCORDANCE WITH 37 CFR §§ 1.97(C) AND 1.98

Pursuant to 37 CFR §§ 1.97(c) and 1.98, Applicant respectfully submits the following statement consisting of:

- A list of documents; and Α.
- General remarks. B.

A copy of the listed document is enclosed herewith along with Forms PTO/SB/08A and PTO/SB/08B.

A. **Documents**

- 1. U.S. Patent No. 4,920,504, issued April, 1990 to Sawada et al.;
- U.S. Patent No. 5,384,737, issued January, 1995 to Childs et al.; 2.
- 3. U.S. Patent No. 5,600,605, issued February, 1997 to Schaefer;
- U.S. Patent No. 5,602,986, issued February, 1997 to Trevett; 4.
- 5. U.S. Patent No. 5,657,469, issued August, 1997 to Shimizu;
- 6. U.S. Patent No. 5,752,269, issued May, 1998 to Divivier et al.;
- 7. U.S. Patent No. 5,761,727, issued June, 1998 to Wu et al.;

- 8. U.S. Patent No. 5,767,865, issued June, 1998 to Inoue et al.;
- 9. U.S. Patent No. 5,815,167, issued September, 1998 to Muthal et al.;
- 10. U.S. Patent No. 5,818,464, issued October, 1998 to Wade;
- 11. U.S. Patent No. 5,822,772, issued October, 1998 to Chan et al.;
- 12. U.S. Patent No. 5,850,483, issued December, 1998 to Takabatake et al.;
- 13. U.S. Patent No. 5,852,451, issued December, 1998 to Cox et al.;
- 14. U.S. Patent No. 5,854,637, issued December, 1998 to Sturges;
- 15. U.S. Patent No. 5,854,638, issued December, 1998 to Tung;
- 16. U.S. Patent No. 5,920,352, issued July, 1999 to Inoue;
- 17. U.S. Patent No. 5,987,574, issued November, 1999 to Paluch;
- 18. U.S. Patent No. 5,996,036, issued November, 1999 to Kelly;
- 19. U.S. Patent No. 6,002,412, issued December, 1999 to Shinnerer;
- 20. U.S. Patent No. 6,041,417, issued March, 2000 to Hammond et al.;
- 21. U.S. Patent No. 6,052,134, issued April, 2000 to Foster;
- 22. U.S. Patent No. 6,052,756, issued April, 2000 to Barnaby et al.;
- 23. U.S. Patent No. 6,076,139, issued June, 2000 to Welker et al.;
- 24. U.S. Patent No. 6,078,338, issued June, 2000 to Horan et al.;
- 25. U.S. Patent No. 6,092,158, issued July, 2000 to Harriman et al.;
- 26. U.S. Patent No. 6,104,416, issued August, 2000 to McGuinness;
- 27. U.S. Patent No. 6,118,462, issued September, 2000 to Margulis;
- 28. U.S. Patent No. 6,151,651, issued November, 2000 to Hewitt et al.;
- 29. U.S. Patent No. 6,173,367 B1, issued January, 2001 to Aleksic et al.;
- 30. U.S. Patent No. 6,199,145 B1, issued March, 2001 to Ajanovic et al.;
- 31. U.S. Patent No. 6,212,611 B1, issued April, 2001 to Nizar et al.;
- 32. U.S. Patent No. 6,230,223 B1, issued May, 2001 to Olarig;

- 33. U.S. Patent No. 6,252,612 B1, issued June, 2001 to Jeddeloh;
- 34. U.S. Patent No. 6,260,123 B1, issued July, 2001 to Strongin et al.;
- 35. U.S. Patent No. 6,269,433 B1, issued July, 2001 to Jones et al.;
- 36. U.S. Patent No. 6,279,065 B1, issued August, 2001 to Chin et al.;
- 37. U.S. Patent No. 6,295,068 B1, issued September, 2001 to Peddada et al.;
- 38. U.S. Patent No. 6,308,237 B1, issued October, 2001 to Strongin et al.;
- 39. U.S. Patent No. 6,317,803 B1, issued November, 2001 to Rasmussen et al.;
 - 40. U.S. Patent No. 6,326,973 B1, issued December, 2001 to Behrbaum et al.;
 - 41. U.S. Patent No. 6,327,636 B1, issued December, 2001 to Guthrie et al.;
 - 42. U.S. Patent No. 6,330,646 B1, issued December, 2001 to Clohset et al.;
 - 43. U.S. Patent No. 6,330,654 B1, issued December, 2001 to LaBerge et al.;
 - 44. U.S. Patent No. 6,381,672 B1, issued April, 2002 to Strongin et al.;
 - 45. U.S. Patent No. 6,412,048 B1, issued June, 2002 to Chauvel et al.;
 - 46. U.S. Patent No. 6,469,703 B1, issued October, 2002 to Aleksic et al.;
 - 47. U.S. Patent No. 6,486,884 B1, issued November, 2002 to Aleksic et al.;
 - 48. U.S. Patent No. 6,504,549 B1, issued January, 2003 to Holister et al.;
 - 49. U.S. Patent No. 6,542,159 B1, issued April, 2003 to Mizuyabu et al.;
 - 50. U.S. Patent No. 6,546,449 B1, issued April, 2003 to Aleksic et al.;
- 51. EP Patent Application No. EP0829820 A2, published March 18, 1998 to Silicon Graphics;
- 52. International Publication No. WO97/06523, published February 20, 1997 to Cirrus Logic;
- 53. European Search Report for Patent Application EP 0030 4528, published May 3, 2002; and
- 54. VLSI Implementation of High Performance Burst Mode for 128 Bit Block Ciphers; Mitsuyama et al.; IEEE.

REMARKS

The submission of the above documents is not an admission that the information is prior art, analogous or otherwise material. It is respectfully requested that the above-listed documents be considered and made of record in the present application.

The Patent Office is hereby authorized to charge the fee of \$180.00 set forth in 37 CFR 1.17(p) to Deposit Account No. 50-0441.

Respectfully submitted,

Dated: 3-23-05

Christopher J. Reckamp

Reg. No. 34,414

Vedder, Price, Kaufman & Kammholz, P.C. 222 North LaSalle Chicago, Illinois 60601 312/609-7500 312/609-5005 Facsimile

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of 4

Complete if Known					
Application Number	10/075,149				
Filing Date	February 14, 2002				
First Named Inventor	Milivoje Aleksic				
Art Unit	2111				
Examiner Name	Justin King				
Attorney Docket Number	00100.02.0060				

			U. S. PATEN	T DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2 (F known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		^{US-} 4,920,504	4-1990	Sawada et a.	
		^{US-} 5,384,737	1-1995	Childs et al.	
		^{US-} 5,600,605	2-1997	Schaefer	
		^{US-} 5,602,986	2-1997	Trevett	
		^{US-} 5,657,469	8-1997	Shimizu	
		^{US-} 5,752,269	5-1998	Divivier et al.	
		^{US-} 5,761,727	6-1998	Wu et al.	
		^{US-} 5,767,865	6-1998	Inoue et al.	
		^{US-} 5,815,167	9-1998	Muthal et al.	
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		^{US-} 5,822,772	10-1998	Chan et al.	
		^{US-} 5,850,483	12-1998	Takabatake et al.	
		^{US-} 5,852,451	12-1998	Cox et al.	
		^{US-} 5,854,637	12-1998	Sturges	
		^{US-} 5,854,638	12-1998	Tung	
		^{US-} 5,920,352	7-1999	Inoue	
		^{US-} 5,987,574	11-1999	Paluch	
		^{US-} 5,996,036	11-1999	Kelly	
		^{US-} 6,002,412	12-1999	Schinnerer	

Examiner Cite Initials* No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	1	
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	ť
		EP 0829820 A2	3-18-1998	Silicon Graphic		
		WO97/06523	2-20-1997	Cirrus Logic		

Examiner	Date	
Signature	Considered	

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Sheet 2

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of 4

Application Number 10/075,149

Filing Date February 14, 2002

First Named Inventor Milivoje Aleksic

Art Unit 2111

Examiner Name Justin King

Attorney Docket Number 00100.02.0060

Examiner Initials*	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No. ¹	Number-Kind Code ^{2 (# known)}	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
		^{US-} 6,041,417	3-2000	Hammond et al.	
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		^{US-} 6,104,416	8-2000	McGuinness	
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-		^{US-} 6,151,651	11-2000	Hewitt et al.	
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		^{US-} 6,230,223 B1	5-2001	Olarig	
		^{US-} 6,252,612 B1	6-2001	Jeddeloh	
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,,		^{US-} 6,269,433 B1	7-2001	Jones et al.	
		^{US-} 6,279,065 B1	8-2001	Chin et al.	
		^{US-} 6,295,068 B1	9-2001	Peddada et al.	
		^{US-} 6,308,237 B1	10-2001	Strongin et al.	

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Sheet 3

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		^{US-} 6,317,803 B1	11-2001	Rasmussen et al.	
		^{US-} 6,326,973 B1	12-2001	Behrbaum et al.	
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		^{US-} 6,330,646 B1	12-2001	Clohset et al.	
		^{US-} 6,330,654 B1	12-2001	LaBerge et al.	
		^{US-} 6,381,672 B1	4-2002	Strongin et al.	
		^{US-} 6,412,048 B1	6-2002	Chauvel et al.	
		^{US-} 6,469,703 B1	10-2002	Aleksic et al.	
		^{US-} 6,486,884 B1	11-2002	Aleksic et al.	
,		^{US-} 6,504,549 B1	1-2003	Holister et al.	
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		^{US-} 6,546,449 B1	4-2003	Aleksic et al.	
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		European Search Report for Patent Application EP 0030 4528, published May 3, 2002.	
		MITSUYAMA et al.; VLSI Implementation of High Performance Burst Mode for 128 Bit Block Ciphers; IEEE	
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